

ABSTRACT

A fast acting charge pump is provided which is suitable for use
5 in a locked loop circuit where very short duration first and second
adjustment pulses are produced by a phase detector. The first
complement of the second adjustment pulses are used to switch the
output of the charge pump through respective pairs of switching and
associated biasing transistors, while a complement of the first and second
10 adjustment pulses are respectively capacitively coupled to interconnection
nodes of the pairs of switching and biasing transistors.

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